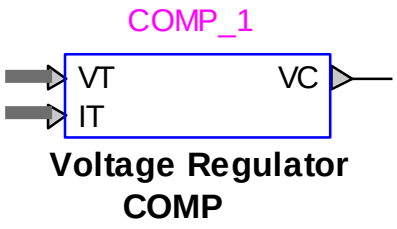
Exciters and Governors: Voltage Regulator COMP



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1 Description

This device is an implementation of the voltage regulator current compensating model COMP. This device is implemented as described in [1]. Implementation details can be viewed by inspecting the subcircuit of this device.

1.1 Pins

This device has 10 pins:

Pin name	Type	Description	Units
VT	Input, bundle	Terminal voltage (phasor) of synchronous machine (magnitude and phase)	pu
IT	Input, bundle	Current (phasor) of synchronous machine (magnitude and phase)	pu
VC	Output	Voltage compensator signal	pu

1.2 Parameters

The default set of parameters can be found in [1].

1.2.1 Data tab

The parameters on the Data tab are:

1. **Reactance X_E**: compensation reactance of synchronous machine

2 References

[1] PSS®E MODEL LIBRARY PSS®E 32.0.5, Siemens Energy, Inc.