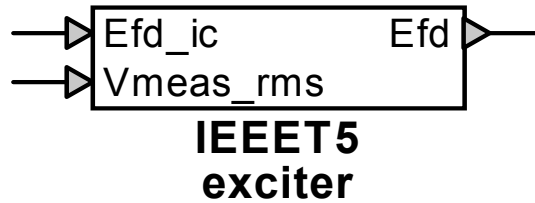


Machine control : exciter IEEEET5



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1 Description

This device is an implementation of an IEEE Type 5 exciter similar to PSS/E's IEEEET5 exciter model. This version of the exciter interprets all input and output values in physical units. For a version with input and output in per-unit quantities, use the device "exciter IEEEET5 pu".

1.1 Pins

This device has three pins:

<i>pin</i>	<i>type</i>	<i>description</i>	<i>units</i>
Efd_ic	input pin	initial field voltage at t=0	V
Vmeas_rms	input pin	measured rms voltage	V
Efd	output pin	field voltage	V

1.2 Parameters

The value of the following parameters must be defined:

<i>parameter</i>	<i>description</i>	<i>units</i>
Trh	1/gain of regulator	s (includes base conversion)
Te	time constant of exciter	s
Ke	gain of exciter	
Kv	regulator zone	pu(V_base)
Vrmin	regulator low limit	pu(Efd_base)
Vrmax	regulator high limit	pu(Efd_base)
E1	E value of point 1 of saturation curve	pu(Efd_base)
S1	S value of point 1 of saturation curve	pu(Efd_base)
E2	E value of point 2 of saturation curve	pu(Efd_base)

S2	S value of point 2 of saturation curve	pu(Efd_base)
V_base	terminal voltage base	V
Efd_base	field voltage base	V

1.3 Input

The input pins may be connected to any control signals.

The following inputs are available:

input	description	units
Efd_ic	initial field voltage at t=0	V
Vmeas_rms	measured rms voltage	V

1.4 Output

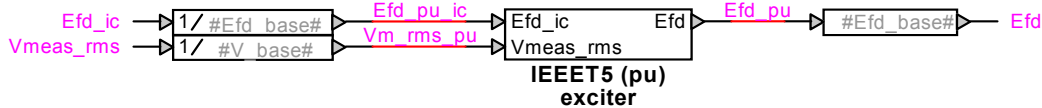
The output value is the calculated field voltage, in physical units.

output	description	units
Efd	field voltage	V

1.5 Representation

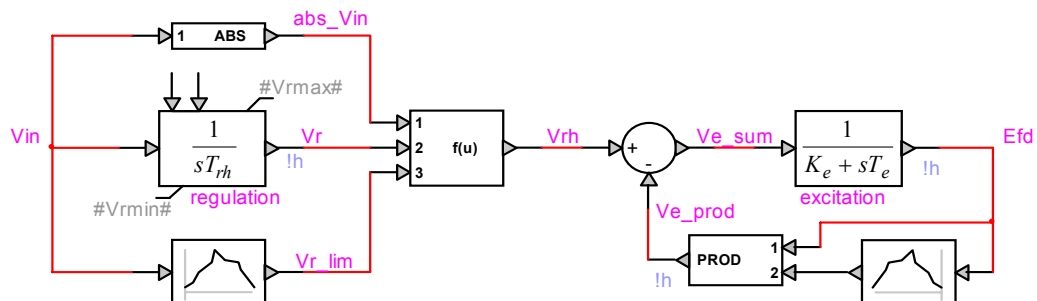
The implementation of the model can be inspected by opening the device's subcircuit.

The model uses a unit-conversion shell surrounding the per-unit version of this exciter.



The model is self-initializing at t=0.

The dynamic representation of the model is the following:



where

$$V_{in} = V_{ref} - V_{meas_rms} \quad (1)$$

$$\begin{aligned}
 V_{r_lim} &= V_{r_min} && \text{when } V_{in} < -K_v \\
 &= V_{r_max} && \text{when } V_{in} > K_v \\
 &= 0 && \text{when } -K_v \leq V_{in} \leq K_v
 \end{aligned}
 \tag{2}$$

$$\begin{aligned}
 V_{rh} &= V_r && \text{when } |V_{in}| \leq K_v \\
 &= V_{r_lim} && \text{when } |V_{in}| > K_v
 \end{aligned}
 \tag{3}$$

with the value of V_{ref} calculated to produce $E_{fd} = E_{fd_ic}$ at $t=0$.

The internal signals are:

<i>signal</i>	<i>description</i>	<i>units</i>
Vin	control input	pu(V_base)
Vr	regulator voltage	pu(Efd_base)
Vr_lim	regulator limit	pu(Efd_base)
Vrh	actual regulator voltage	pu(Efd_base)