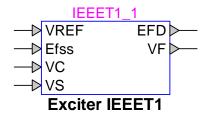
Exciters and Governors: Exciter IEEET1



 Exciters and Governors: Exciter IEEET1
 1

 1 Description
 1

 1.1 Pins
 1

 1.2 Parameters
 1

 1.2.1 Data tab
 1

 1.2.2 Exciter tab
 2

 2 Initial conditions
 2

 3 References
 2

Tshibain Tshibungu, Jean Mahseredjian, 12/15/2016 11:00 AM

1 Description

This device is an implementation of the IEEE type IEEET1 excitation system model. This device is implemented as described in [1]. Implementation details can be viewed by inspecting the subcircuit of this device.

1.1 Pins

This device has 6 pins:

Pin name	Туре	Description	Units
VREF	Input	Reference voltage of the stator terminal voltage	pu
Efss	Input	Steady-state field voltage at t = 0, for initialization	pu
VC	Input	Terminal voltage of synchronous machine,	pu
		transducer output	
VS	Input	Power System Stabilizer signal	pu
EFD	Output	The field voltage signal	pu
VF	Output	The excitation system stabilizer signal	pu

1.2 Parameters

The default set of parameters can be found in [1].

1.2.1 Data tab

The parameters on the Data tab are:

- 1. Gain K_A: voltage regulator gain
- 2. Time constant T_A: voltage regulator time constant
- 3. Maximum regulator output V_{Rmax}: Maximum regulator voltage output
- 4. **Minimum regulator output V**_{Rmin}: Minimum regulator voltage output
- 5. Gain K_F: damping filter gain

6. **Time constant T**_F: damping filter time constant

1.2.2 Exciter tab

The exciter tab allows to input:

- 1. Gain K_E: exciter gain
- 2. Time constant T_E: exciter time constant
- 3. Field voltage E_{FD1}: The field exciter voltage point which is near the exciter ceiling voltage
- 4. Field voltage E_{FD2}: The field exciter voltage point which is near 75% of E_{FD1}
- 5. Saturation function output SE_E_{FD1}: The exciter saturation function value at E_{FD1}
- 6. Saturation function output SE_EFD2: The exciter saturation function value at EFD2

2 Initial conditions

The reference voltage VREF can be manually or automatically set by connecting or not connecting the input signal VREF, respectively. When VREF is not connected (the signal is zero), the reference voltage is internally found from the steady-state solution. When VREF is connected, its initial value must match the per unit steady-state voltage of the stator terminal voltage, since otherwise the generator voltage will not start at the actual steady-state.

3 References

[1] "Computer Representation of Excitation Systems," IEEE COMMITTEE REPORT 1968.